

Notice of Allowability	Application No.	Applicant(s)
	10/806,638	TAN ET AL.
	Examiner	Art Unit
	Pho M. Luu	2824

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTO-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to Amendment filed on 10/12/05.
2. The allowed claim(s) is/are 1-23.
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 of the:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____

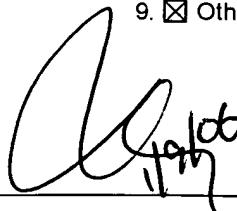
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of
 Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date _____
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other Search History.



RICHARD ELMS
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800

DETAILED ACTION

Reply to Restriction Requirement

1. Acknowledgment is made of application's Reply to Restriction Requirement, with traverse, filed 12 October 2005. However, upon further consideration in view of applicant's transversal therein, the restriction requirement has been withdrawn. Therefore, claims 1-23 are pending in the application.

2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

In abstract: Lines 13, delete "60125832 v3".

 Page 6, line 31: replace "425" with --420--. *(In the Specification)*

~~Appropriate correction is required.~~

3. Claims 1-23 is allowed.

The following is an examiner's statement of reasons for allowance:

There is no teaching or suggestion in the prior art to provide:

"a memory partitioning set including at least one memory partitioning circuit wherein the memory partitioning circuit is connected between an address line of one of the sets of address lines and a corresponding input of one of the set of memory ports, and wherein the memory partitioning circuit includes a first operation mode adapted to hold the corresponding input of one of the set of

memory ports to a fixed value and a second operation mode adapted to pass the value of the connected address line to the corresponding input of one of the set of memory ports” in a memory cell as claimed in the independent claim 1. The dependent claims 2-6 are also allowed; or

“a memory partitioning set of at least one memory partitioning circuit connected between an address line of one of the sets of address lines and a corresponding input of one of the set of memory ports, wherein the memory partitioning circuit includes a first operation mode adapted to hold the corresponding input of one of set of memory ports to a fixed value and a second mode of operation adapted to pass the value of the connected address line to the corresponding input of one of the set of memory ports” in a programmable devices as claimed in the independent claim 7. The dependent claims 8-12 are also allowed; or

“a memory partitioning set of at least one memory partitioning circuit connected between an address line of one of the sets of address lines and a corresponding input of one of the set of memory ports, wherein the memory partitioning circuit includes a first operation mode adapted to hold the corresponding input of one of set of memory ports to a fixed value and a second mode of operation adapted to pass the value of the connected address line to the corresponding input of one of the set of memory ports” in a system having a plurality of devices as claimed in the independent claim 13. The dependent claims 14-18 are also allowed; or

“a third portion adapted to configure a set of memory partitioning circuits associated with a memory cell of the programmable device, wherein each of the set of memory partitioning circuits has a first operation mode adapted to hold an input of a first memory port of a multiple port memory device of the memory cell to a fixed value and a second operation mode adapted to pass a value of an address line to the input of a first memory port of a multiple port memory device of the memory cell” of an information storage medium including a device configuration specifying a configuration of a programmable device as claimed in the independent claim 19. The dependent claims 20-23 are also allowed.

4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Tagaya (US. 5,325,332) disclosed a dual port semiconductor memory device include redundancy circuit set the address signal code word in the counter circuit as well as a shift register included in the dual port memory as their initial value in a serial read mode.

6. Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Pho M. Luu whose telephone number is 571.272.1876. The examiners can normally be reached on M-F 8:00AM – 5:00PM.

If attempts to reach the Examiner by telephone are unsuccessful, the Examiner's Supervisor, Richard Elms, can be reached on 571.272.1869. The official fax number for the organization where this application or proceeding is assigned is 703.872.9306 for all official communications.

The information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

PML
22 December 2005